

Figure 1

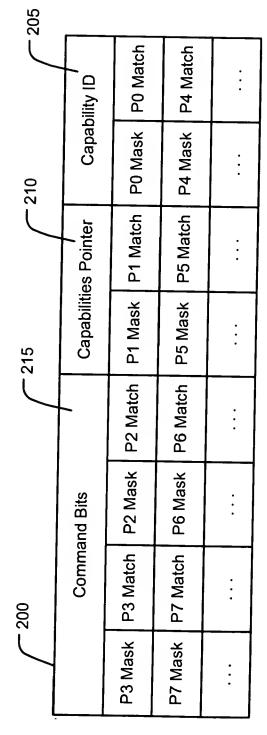


Figure 2

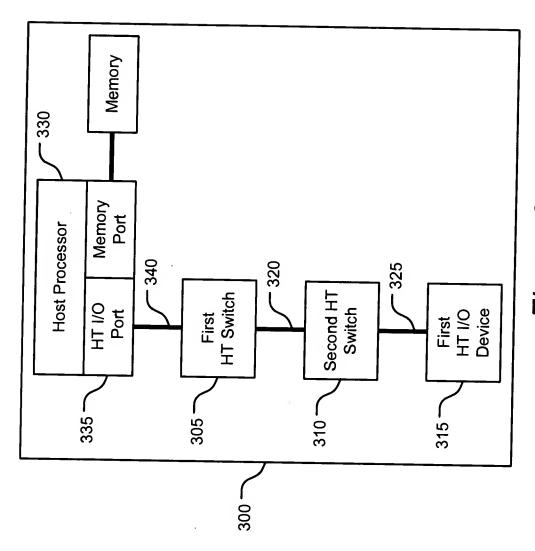
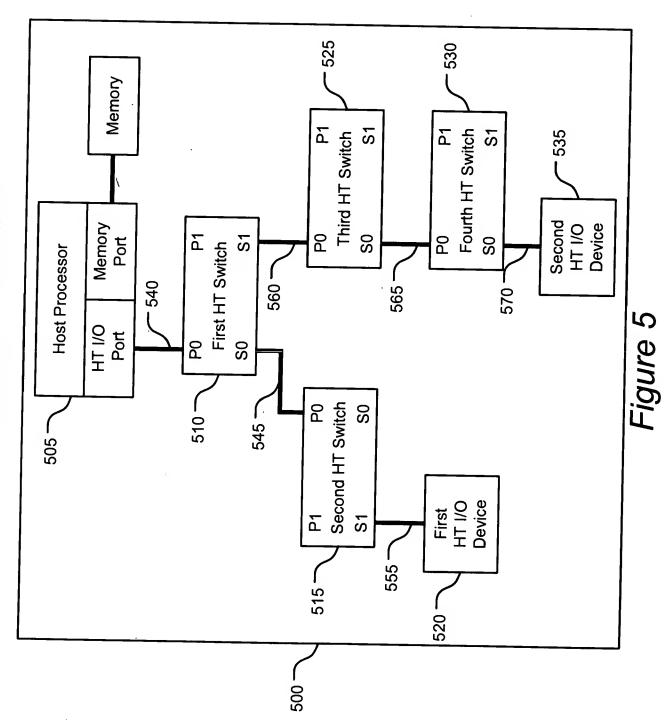


Figure 3

		T	_	_	
	Capability ID		S0 Match	S4 Match	
	Capat		SU Mask	S4 Mask	
	Capabilities Pointer	S3 Mask S3 Match S2 Match S4 Mack S4 Match	o i Match	S7 Mask S7 Match S6 Match S5 Mask S5 Match S4 Mask S4 Match	
	Capabilitie	C4 Mack	O Mask	S5 Mask	
	Command Bits	S2 Match	כב ייומיטו	S6 Match	
		S2 Mask		S6 Mask	
		S3 Match		S7 Match	
		S3 Mask		S7 Mask	

Figure 4



		Primary Segment		Primary Segment
Capability ID	Unit ID 0	Prir	Unit ID 4	
Capa	Ğ	Secondary Segment	Unii	Secondary Segment
Capabilities Pointer	Unit ID 1	Primary Segment	Unit ID 5	Primary Segment
Capabiliti	Unit	Secondary Segment	Unit	Secondary Segment
	Unit ID 2	Primary Segment	Unit ID 6	Primary Segment
Command Bits	Unit	Secondary Segment	Unit	Secondary Segment
Commi	Unit ID 3	Primary Segment	Unit ID 7	Primary Segment
	Unit	Secondary Segment	Unit	Secondary Segment

009\_

Figure 6

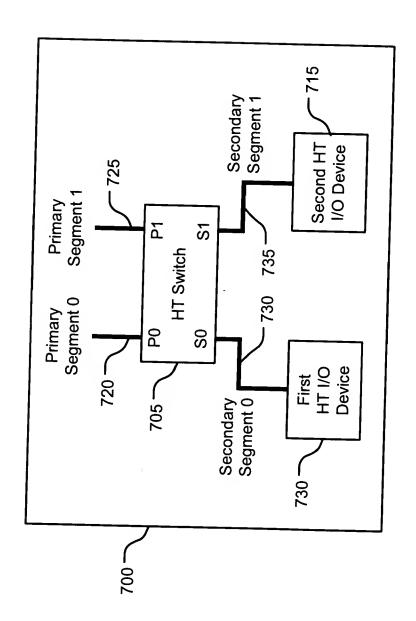
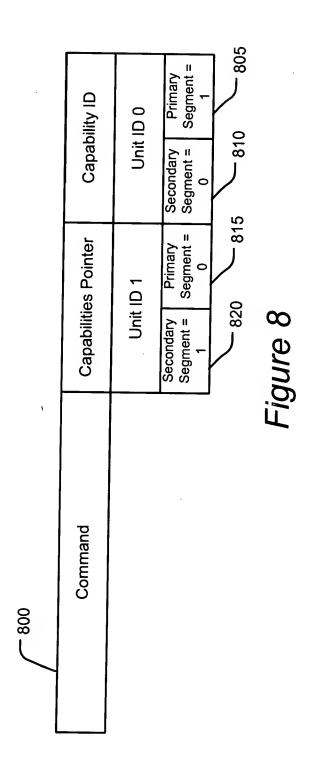
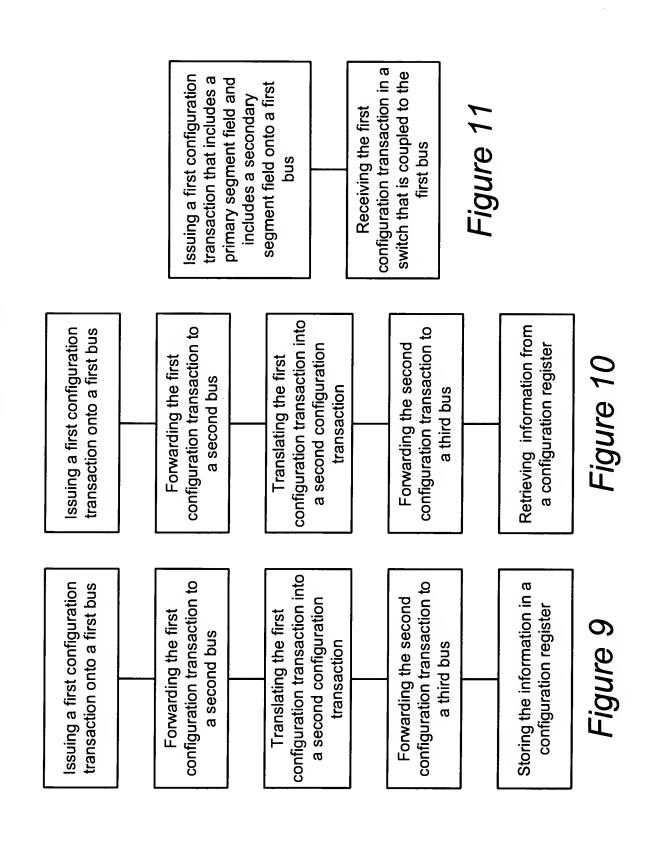
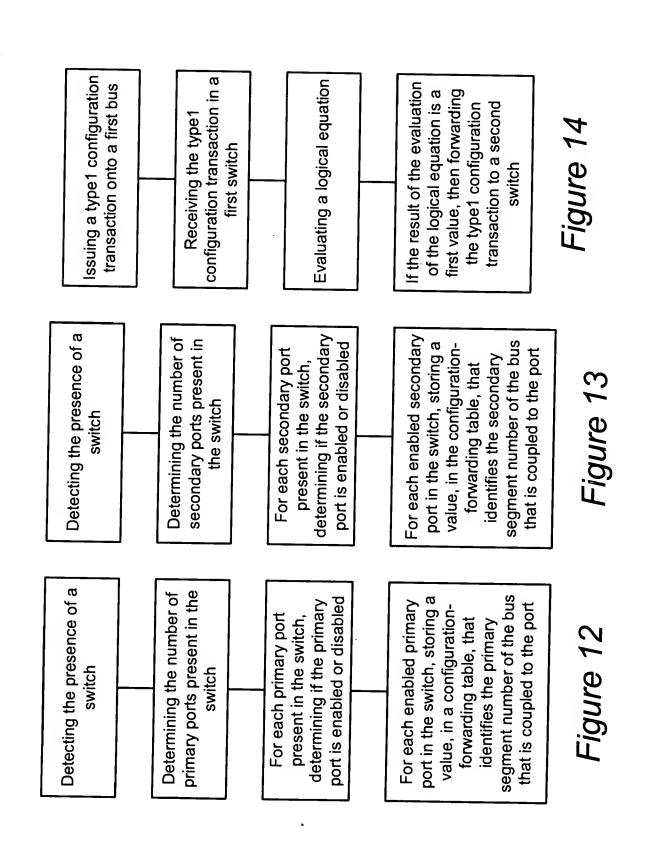
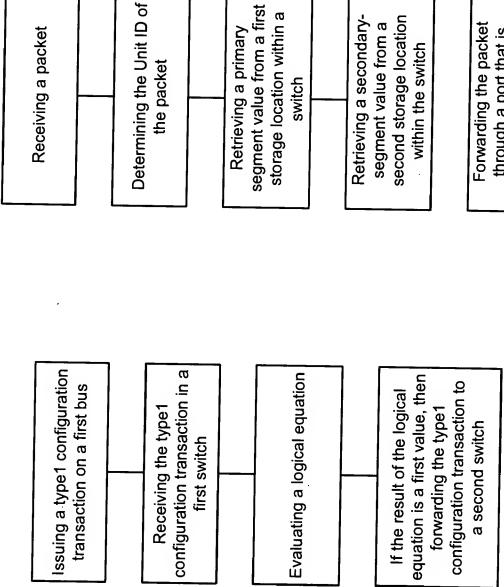


Figure 7









through a port that is coupled to a bus that is identified by the primary-segment value secondary-segment value

Figure 15

## Figure 16